

COMMUNICATION APPARATUS, COMMUNICATION SYSTEM
AND METHOD FOR INTEGRATING SPEECH AND DATA

BACKGROUND OF THE INVENTION

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(1) Field of the Invention

This invention relates to a communication apparatus,
communication system, and method for integrating speech and
data and, more particularly, to a communication apparatus
10 for communicating by integrating speech and data,
communication system for communicating by integrating
speech and data, and method for integrating speech and data
for communicating by integrating and controlling speech and
data.

15 (2) Description of the Related Art

In recent years, in addition to conventional
centralized telephone switching service, the use of high-
speed transmission via distributed LAN for local
communication is increasing. As a result, intranet
20 communication is widely performed. Conventionally,
installations for such an in-house PBX network and intranet
have been constructed separately from each other because of
their difference in protocol. Conventionally, a public
telephone trunk network and internet trunk network have
25 also been installed separately from each other.

A telephone network and data network are installed
separately from each other in this way, resulting in low

SUMMARY OF THE INVENTION

An object of the present invention is to provide a communication apparatus for performing high-quality communication by integrating speech and data efficiently.

5 Another object of the present invention is to provide a communication system for performing high-quality communication by integrating speech and data efficiently.

Still another object of the present invention is to provide a method for integrating speech and data for
10 performing high-quality communication by integrating speech and data efficiently.

In order to achieve the above object, a communication apparatus for communicating speech and data is provided. This communication apparatus comprises frame
15 controlling means for integrating speech frames, being speech signals made into frames, and data frames, being data made into frames, into integrated frames and controlling the routing of the speech frames, the data frames, and the integrated frames, input processing means
20 for storing and managing at least one of the three types of frames of the speech frames, the data frames, and the integrated frames, and output processing means for storing and managing the frames transferred, setting a bandwidth ratio of the frames dynamically, and transmitting the
25 speech frames, the data frames, and the integrated frames from on lines.

Furthermore, in order to achieve the above object,

types of frames of the speech frames, the data frames, and the integrated frames, the step of controlling the routing of the speech frames, the data frames, and the integrated frames, and the step as an output-side process of storing and managing the frames transferred, setting a bandwidth ratio of the frames dynamically, and transmitting the speech frames, the data frames, and the integrated frames from on lines.

The above and other objects, features and advantages of the present invention will become apparent from the following description when taken in conjunction with the accompanying drawings which illustrate preferred embodiments of the present invention by way of example.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a view for describing the principle of a communication apparatus according to the present invention.

Fig. 2 is a view showing the configuration of a communication system.

Fig. 3 is a view showing a model for network configuration.

Fig. 4 are views showing the structure of frames.

Fig. 5 is a view showing the structure of an integrated frame in detail.

Fig. 6 is a schematic showing a process for transferring frames.

Fig. 7 is a view showing the configuration of a

communication apparatus.

Fig. 8 is a view showing the configuration of a communication apparatus.

Fig. 9 is a view roughly showing a process performed when identical IP frames exist.

Fig. 10 is a flow chart showing the procedure of processes performed in a method for integrating speech and data according to the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENT

An embodiment of the present invention will now be described with reference to the drawings. Fig. 1 is a view for describing the principle of a communication apparatus according to the present invention. A communication apparatus 10 communicates speech and data. In the present invention, speech means a speech signal on a telephone line and data means Internet protocol (IP) data.

Frame controlling means 12 integrates speech frames, being speech signals made into frames, and data frames, data made into frames, into integrated frames and controls the routing of speech frames, data frames, and integrated frames.

Input processing means 11 stores and manages at least one type of speech frames, data frames, and integrated frames. In Fig. 1 speech and data frames are input. However, if the communication apparatus 10 connects with another communication apparatus and this communication apparatus sends integrated frames, they will also be input

to the input processing means 11.

Output processing means 13 stores and manages these frames transferred. In addition, it sets a bandwidth ratio of frames dynamically and transmits speech frames, data frames, and integrated frames from on lines.

In Fig. 1, speech frame bandwidth is assigned to line L1 and speech frames are transmitted via line L1; data frame bandwidth is assigned to line L2 and data frames are transmitted via line L2.

Moreover, integrated frame bandwidth is assigned to line L3 and integrated frames are transmitted via line L3. That is to say, signals in which speech and data mix are transmitted via line L3.

There is little telephone traffic at night-time. In this case the output processing means 13 can assign data frame bandwidth dynamically to, for example, line L1 to which speech frame bandwidth is assigned in the daytime. Volumes of data can be transferred in this way, which enables flexible transmission control.

A communication system according to the present invention in which the communication apparatus 10 is applied will now be described. Fig. 2 is a view showing the configuration of a communication system. A communication system 10a comprises a telephone switching system 102, a network connecting device 105 (router 105 in Fig. 2) corresponding to a router or gateway, and the communication apparatus 10.

312 via data interface (IP) line L2; the communication apparatus 10 in the station 100 connects with a communication apparatus 10-3 in the station 313 via integrated interface (Telephone IP: TIP) line L3.

5 In this case, for example, office telephone interface line If1 and office data interface line If2 each accommodate 622-Mbps OC12/STM-4 interface.

Speech interface line L1 accommodates 156-Mbps OC3/STM-1 interface; data interface line L2 and integrated
10 interface line L3 each accommodate 622-Mbps OC12/STM-4 interface.

Fig. 3 is a view showing a model for network configuration. In Fig. 3, black circles indicate stations having a local switch (LS) and black squares indicate
15 stations having a toll switch (TS). Furthermore, all stations include the communication apparatus 10 according to the present invention.

Stations 121-125 at LS stage are exchanges for accommodating subscribers, have a double loop configuration
20 (currently-used/spare connection configuration), and connect with subscriber terminals 2a-2e respectively.

Stations 131-134 at TS stage are exchanges having a relay function and have a double loop configuration. The stations 131 and 132 each connect with the stations 121-125.
25 A regional center 135 is an exchange for managing stations in an network and connects with the stations 131-134.

Now, the structure of frames output from the

communication apparatus 10 will be described. Fig. 4 are views showing the structure of frames. Figs. 4(A), 4(B), and 4(C) indicate the structure of integrated frame F2 on integrated interface line L3, speech frame F3 on speech interface line L1, and data frame (IP frame) F4 on data interface line L2 respectively.

Integrated frame F2 shown in Fig. 4(A) consists of a header 21, a speech frame 22, and an IP frame 23. In integrated frame F2, bandwidth used actually for a telephone line is occupied by the speech frame 22 and the rest of the bandwidth is assigned to the IP frame 23.

In addition, the IP frame 23 consists of, for example, three IP packets in one cycle and QOS priority for each packet is set. This priority information is set in the header 21 (described later in Fig. 5).

Speech frame F3 shown in Fig. 4(B) is a frame signal generated by making telephone speech into a frame. IP frame F4 shown in Fig. 4(C) consists of a plurality of IP packets.

Now, the structure of integrated frame F2 will be described in detail. Fig. 5 is a view showing the structure of integrated frame F2 in detail. Integrated frame F2 consists of the header (HD) 21, the speech frame (TS0 and TS1) 22, the IP frame (IP0, IP1, and IP2) 23, and a tail (TL) 24 with CRC indicating the tail of a frame.

In this case, TS0 is a speech frame for an upper telephone line and TS1 is a speech frame for an ordinary

telephone line. IPn (IP0 through IP2) is an IP packet frame with priority "n." For example, priority "0" corresponds to bandwidth-guaranteed connection-mode communication and priority "1" and "2" correspond to non-
5 bandwidth-guaranteed connectionless-mode communication (best-effort etc.).

In the header 21, SYNC is a synchronizing pattern; PLL is payload length; HDL is header length; CCS is a control field for No. 7 common line signaling system for
10 telephone and IP. CCS enables to ensure bandwidth for control signals for speech and IP and to transmit them without being influenced by traffic.

TS-IDX is a telephone frame index. TS0-Slot-IDX is an upper telephone slot-state index. For example, "00,"
15 "01," "10," and "11" indicate "inactive/shut down," "inactive/shut down for maintenance," "active and unused," and "active and used" respectively. TS1-Slot-IDX is an ordinary telephone slot-state index. For example, "00," "01," "10," and "11" indicate "inactive/shut down,"
20 "inactive/shut down for maintenance," "active and unused," and "active and used" respectively.

IP-IDX and IPn-IDX correspond to priority information fields. IP-IDX is an IP frame index; IPn-IDX is an nth-priority IP packet index. HD-CRC is header CRC.

25 As described above, the communication apparatus 10 according to the present invention controls the routing of speech frames, IP frames, and integrated frames, and

transmits speech frames, IP frames, and integrated frames via speech interface line L1, data interface line L2, and integrated interface line L3 respectively.

Furthermore, speech frames are guaranteed bandwidth 5 which they use, and the transmission of IP frames is controlled on the basis of priority as a semi-bandwidth guarantee.

This improves speech communication quality by comparison with conventional Internet telephone and enables 10 to communicate by integrating speech and IP frames.

Moreover, by configuring the communication system 10a according to the present invention by connecting the communication apparatus 10 according to the present invention to office telephone interface line If1, office 15 data interface line If2, and interoffice trunk lines, speech communication, data communication, and speech-data communication in which speech and data are integrated can be performed easily. In addition, by making good use of the existing network systems, new services can be provided 20 with the increase of installations minimized.

The process of transferring frames will now be described. Fig. 6 is a schematic showing a process for transferring frames. Black circles, white circles, black squares, white squares, and diamonds in Fig. 6 indicate 25 upper speech frames, ordinary speech frames, first-priority IP frames, second-priority IP frames, and third-priority IP frames respectively.

Input processing means 11-1 which stores speech frames has a storage area consisting of A and B sides. It is assumed that A and B sides are receiving buffers 11-1a and 11-1b respectively. Furthermore, in Fig. 6 the receiving buffers 11-1a and 11-1b each have a storage area for two lines.

Similarly, input processing means 11-2 which stores IP frames consists of receiving buffers 11-2a and 11-2b and the receiving buffers 11-2a and 11-2b each have a storage area for two lines. Input processing means 11-3 which stores integrated frames consists of receiving buffers 11-3a and 11-3b and the receiving buffers 11-3a and 11-3b each have a storage area for two lines.

Sides of the above receiving buffers 11-1a through 11-3a and 11-1b through 11-3b are switched at cycles of, for example, 2 milliseconds. The cycle of each frame therefore is also 2 milliseconds.

On the other hand, output processing means 13-1 which stores and outputs speech frames transferred from the frame controlling means 12 has storage areas TEL#1 through TEL#4 for four lines. Similarly, output processing means 13-2 which stores and outputs IP frames transferred from the frame controlling means 12 has storage areas IP#1 through IP#4 for four lines; output processing means 13-3 which stores and outputs integrated frames transferred from the frame controlling means 12 has storage areas TIP#1 through TIP#4 for four lines.

IF1 through IF3 in Fig. 6 indicate the flow of frames on lines. IF1 indicates that upper speech frames and ordinary speech frames are transmitted via speech interface line L1 in that order.

5 IF2 indicates that first-priority IP frames, second-priority IP frames, and third-priority IP frames are transmitted via data interface line L2 in that order.

IF3 indicates that upper speech frames, ordinary speech frames, first-priority IP frames, second-priority IP
10 frames, and third-priority IP frames are transmitted via integrated interface line L3 in that order.

The process of transferring frames is performed in the following way. Speech frames in storage area r1 of the receiving buffer 11-1a are transferred to storage area
15 TIP#1 of the output processing means 13-3 by the frame controlling means 12. IP frames in storage area r2 of the receiving buffer 11-2a are transferred to storage area TIP#1 of the output processing means 13-3.

When frames are transferred, the frame controlling
20 means 12 generates integrated frames by adding necessary header information. These integrated frames are stored in storage area TIP#1 of the output processing means 13-3.

The output processing means 13-3 then sets bandwidth dynamically with the traffic on integrated
25 interface line L3 taken into consideration and outputs the integrated frames. The process of transferring other frames is also performed in the same way, so descriptions

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regarding input frames (including a destination and
corresponding to header information described in Fig. 5)
stored in the input frame storing means 113. The internal
configuration of the input processing means 11-2 through
5 11-n is the same as that of the input processing means 11-1,
so descriptions of them will be omitted.

In the output processing means 13-1, output
interface means 131 controls an interface in order to send
output frames, being speech frames, IP frames, or
10 integrated frames, from on a line.

An output CPU 132 controls all the inside of the
output processing means 13-1. Output frame storing means
133 stores output frames sent from the frame controlling
means 12-1. In addition, the output frame storing means
15 133 sends output frames to the output interface means 131
in accordance with an instruction from the output CPU 132.

DMA controlling means 134 sends the frame
controlling means 12-1 information regarding output frames
stored in the output frame storing means 133. The internal
20 configuration of the output processing means 13-2 through
13-n is the same as that of the output processing means 13-
1, so descriptions of them will be omitted.

The frame controlling means 12-1 consists of switch
controlling means 12a and switching means 12b. The switch
25 controlling means 12a controls the routing of frames
(controls routing in order to transfer frames from input
processing means to the corresponding output processing

means) and the switching means 12b on the basis of frame information sent from the DMA controlling means 114 and 134. The switching means 12b performs a switching transfer of frames in accordance with an instruction from the switch
5 controlling means 12a.

As described above, the frame controlling means 12-1 of the communication apparatus 1a controls routing on the basis of frame information and performs a switching transfer of frames to one of the output processing means
10 13-1 through 13-n. This enables communication in which speech and data are integrated efficiently.

A modification of the communication apparatus 1a will now be described. Fig. 8 is a view showing the configuration of a communication apparatus. A
15 communication apparatus 1b, being a modification of the communication apparatus 1a, consists of input processing means 11a-1 through 11a-n, frame controlling means 12-2, and output processing means 13a-1 through 13a-n.

In the input processing means 11a-1, the input
20 interface means 111 controls an interface in order to receive input frames, being speech frames, IP frames, or integrated frames.

The input CPU 112 controls all the inside of the input processing means 11a-1. The input frame storing
25 means 113 consists of two receiving buffers, as described in Fig. 6, and stores input frames. In addition, the input frame storing means 113 sends input frames and frame

information to frame controlling means 12-2 in accordance with an instruction from the input CPU 112. The internal configuration of the input processing means 11a-2 through 11a-n is the same as that of the input processing means 11a-1, so descriptions of them will be omitted.

In the output processing means 13a-1, the output interface means 131 controls an interface in order to send output frames, being speech frames, IP frames, or integrated frames, from on a line.

The output CPU 132 controls all the inside of the output processing means 13a-1. The output frame storing means 133 stores output frames sent from the frame controlling means 12-2. In addition, the output frame storing means 133 sends output frames to the output interface means 131 in accordance with an instruction from the output CPU 132. The internal configuration of the output processing means 13a-2 through 13a-n is the same as that of the output processing means 13a-1, so descriptions of them will be omitted.

The frame controlling means 12-2 includes transfer scheduling information generating means 200. The transfer scheduling information generating means 200 performs routing control (controls routing in order to transfer frames from input processing means to the corresponding output processing means) and generates transfer scheduling information t1 through tn regarding routes, on the basis of frame information sent from the input processing means 11a-

1 through 11a-n.

In order to transfer frames, first the input processing means 11a-1 sends the transfer scheduling information generating means 200 information regarding
5 frames stored in the input frame storing means 113.

When the transfer scheduling information generating means 200 receives the frame information, it generates transfer scheduling information t1 and returns transfer scheduling information t1 to the input processing means
10 11a-1.

The input processing means 11a-1 receives transfer scheduling information t1. Then the input processing means 11a-1 determines, from the contents of transfer scheduling information t1, the output processing means to which frames
15 stored in the input frame storing means 113 should be sent. After the determination, the input processing means 11a-1 sends them to the corresponding output processing means via the frame controlling means 12-2. A transfer of frames by the input processing means 11a-2 through 11a-n is performed
20 in the same way, so descriptions of it will be omitted.

In the communication apparatus 1b according to the present invention, as described above, the transfer scheduling information generating means 200 in the frame controlling means 12-2 generates transfer scheduling
25 information t1 through tn on the basis of frame information. Then frames are transferred from the input processing means 11a-1 through 11a-n to the corresponding output processing

line. Then frame controlling means at the receiving end restores the representative frame to a plurality of identical frames as they were. This enables to reduce the traffic on an interoffice trunk line.

5 A method for integrating speech and data according to the present invention will now be described. Fig. 10 is a flow chart showing the procedure of processes performed in a method for integrating speech and data according to the present invention.

10 [S1] Integrated frames are generated by integrating speech frames, being speech signals made into frames, and data frames, data made into frames.

 When integrated frames are generated, speech frames occupy the bandwidth which they use, and the rest of the bandwidth is assigned to data frames. Furthermore, when
15 integrated frames are generated, priority information fields for data frames are created. In that case, high and low priority correspond to connection-mode and connectionless-mode communication respectively. In
20 addition, when integrated frames are generated, an information field for controlling common line signals is created.

 [S2] The input side stores and manages at least one of the three types of frames of speech frames, data frames,
25 and integrated frames. In this case, frames are stored by switching two storage areas every cycle.

 [S3] The routing of speech frames, data frames, and

those skilled in the art, it is not desired to limit the invention to the exact construction and applications shown and described, and accordingly, all suitable modifications and equivalents may be regarded as falling within the scope
5 of the invention in the appended claims and their equivalents.

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